

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application.

LISTING OF CLAIMS:

1. (Currently Amended) A method of conserving power in a WLAN receiver having a module for channel estimation only and a module for pilot processing only of a plurality of packets, said method comprising the steps of:

enabling a said channel estimator module only during the preamble of each packet; and

providing a plurality of channel estimator values;

storing said plurality of channel estimator values; and

enabling pilot processing module for the duration of the packet after the preamble of each packet using said plurality of stored channel estimator values for the duration of the packet.

2. (Currently Amended) A method conserving power in a WLAN receiver of claim 1 comprising the steps of:

enabling a channel estimator only during the preamble of each packet and running wherein enabling pilot processing for the duration of the packet after the preamble of each packet comprising:

providing a plurality of channel estimation pilot processing values; and

to get providing off set error values from the plurality of channel estimation pilot processing values.

3. (Cancelled).

4. (Currently Amended) The method of claim 32 including the step of summing said stored channel estimator values for the duration of the packet with said off set error values determined by the pilot processing.
5. (Currently Amended) A system for conserving power in a WLAN receiver comprising:
 - an equalizer;
 - a dedicated channel estimator enabled only during the preamble of each packet for detecting transmitted errors in a transmitted packet and providing equalization to said equalizer for the detected channel errors; and
 - means response to the start of each packet for enabling said channel estimator during the preamble and thereafter disabling said channel estimator for the remainder of the packet; and
 - store for storing the estimated value for the duration of the packet.
6. (Currently Amended) The system of claim 5 includingfurther comprising a separate pilot processor for detecting off set errors from the channel estimation and providing off set correction to said equalizer for the whole data portion of the packet after the preamble.
7. (Original) The system of claim 6 wherein said stored channel estimator values for the duration of the packet is summed with said off set error values determined by the pilot processing.
8. (Original) The system of claim 7 wherein said equalizer includes a frequency domain equalizer.
9. (Original) The system of claim 7 wherein said equalizer includes a time domain equalizer.

10. (Original) The system of claim 7 wherein said equalizer is a frequency domain equalizer and a time domain equalizer.

11. (Original) The system of claim 6 wherein said equalizer is a frequency domain equalizer and a time domain equalizer.

12. (Original) The system of claim 5 wherein said means for enabling and disabling said channel estimator includes a means for decoding the status of the receiver state machine.

13, (Original) The system of claim 6 wherein said means for enabling and disabling said channel estimator and enabling said separate pilot processing equalizer for the whole data portion of the packet after the preamble includes a means for decoding the status of the receiver state machine.